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Call for Papers

The IEEE International Symposium on Design and Diagnostics of Electronic Circuits and Systems provides a forum for exchanging ideas, discussing research results, and presenting practical applications in the areas of design, test and diagnosis of electronic circuits and systems. The 29th edition of DDECS Symposium will be held in Bratislava, the capital of Slovakia.

Topics and Submissions

Authors are invited to submit original, unpublished contributions in the following (but not limited to) topics:

Digital Circuit Design and Next Generation Chips

Chair:
Sara Vinco
Politecnico di Torino, IT

Verification and Test

Chair:
Georschwin Fey
Hamburg University of Technology, DE

Analog Circuit Design and Test

Chair:
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FEI STU Bratislava, SK

Emerging Technologies Design, Test and Diagnostics

Chair:
Jie Han
University of Alberta, CAN

DDECS 2026 seeks for original papers of the following types:

- **Regular Papers** (6 pages) presenting novel and completed research work
- **Student Papers** (4 Pages) presenting on-going research

Papers will be submitted through ConfTool website: <https://www.conftool.org/ddecs2026/>

DDECS 2026 will produce Formal Proceedings of accepted papers that will be included in the IEEE Xplore Digital Library.

Important Dates

Paper submission:	11 January 2026
Notification of acceptance:	27 February 2026
Camery-ready papers:	16 March 2026



Conference Location

Bratislava is the capital and largest city of Slovakia and the fourth largest of all cities on the river Danube. Bratislava is the political, cultural and economic centre of the country. It is the seat of the Slovak president, the Parliament and the Slovak Executive. It has several universities, and many museums, theatres, galleries and other cultural and educational institutions. Many companies and financial institutions have headquarters there.

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